

PATENT NUMBER

U.S. UTILITY Patent Application

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| O.I.P.E. | PATENT DATE |
| TR. <u>S²</u> SCANNED <u>S²</u> Q.A. <u>LR</u> | |

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| APPLICATION NO. 09/716977 | CONT/PRIOR | CLASS 375 | SUBCLASS 376 | ART UNIT 2634.31 | EXAMINER ZHENG FAN |
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| TITLE | APPLICANTS |
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Alfred Dunlop
Wilhelm Fischer.

4. Gated clock recovery circuit.

PTO-2040
12/89

ISSUING CLASSIFICATION

| ORIGINAL | | | | | | CROSS REFERENCE(S) | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
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| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
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